

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
17 February 2005 (17.02.2005)

PCT

(10) International Publication Number
WO 2005/015704 A3

(51) International Patent Classification⁷: **H02H 5/00**

[US/US]; Kirchplatz 5, 58332 Schwelm (DE). **HOLD**,
Klaus [US/US]; Hittorfstrasse 20, 42897 Remscheid (DE).

(21) International Application Number:
PCT/US2004/025481

(74) Agent: **WOOD, David, P.**; Delphi Technologies, Inc., P.O.
Box 5052, Mail Code: 480-410-202, Troy, MI 48007-5052
(US).

(22) International Filing Date: 6 August 2004 (06.08.2004)

(25) Filing Language: English

(81) Designated States (*unless otherwise indicated, for every
kind of national protection available*): AE, AG, AL, AM,
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,
MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG,
PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM,
TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM,
ZW.

(26) Publication Language: English

(30) Priority Data:
60/493,499 8 August 2003 (08.08.2003) US

(71) Applicant (*for all designated States except US*): **DELPHI
TECHNOLOGIES, INC.** [US/US]; P.O. Box 5052, Mail
code 480-410-202, Troy, MI 48007-5052 (US).

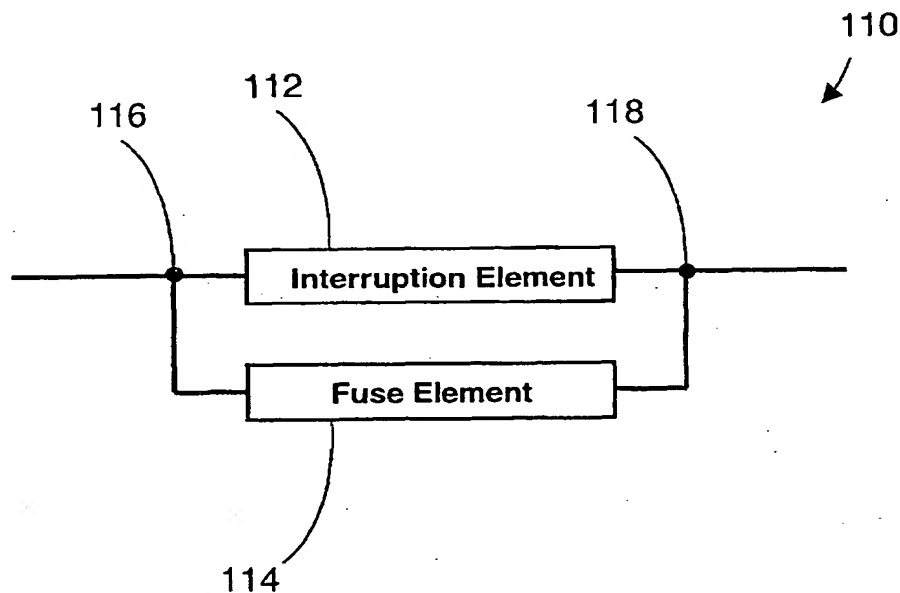
(72) Inventors; and

(75) Inventors/Applicants (*for US only*): **GEORGE, Terry,**
A. [US/US]; 5655 West Garfield Road, Salem, OH 44460
(US). **KLEJA, Fran, A.** [US/US]; 7071 Ruby Courts,
Austintown, OH 44515 (US). **ROSEMANN, Achim**

(84) Designated States (*unless otherwise indicated, for every
kind of regional protection available*): ARIPO (BW, GI,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,

[Continued on next page]

(54) Title: CIRCUIT INTERRUPTION DEVICE



(57) Abstract: A circuit interruption device (110) is provided in which an interruption element (112) and a fuse element (114) are configured electrically in parallel. The interruption element includes an electrical conductor and a current interrupter capable of severing the electrical conductor to eliminate its capacity to carry current. An exemplary embodiment of a circuit interruption device is capable of detecting an interrupt control signal and actuating in response to the signal.

WO 2005/015704 A3



SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

(88) Date of publication of the international search report:
8 December 2005

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.